

Abstract

A method for transferring data via a data bus (124), in particular via a PCI bus (124), is described. The method according to the present invention provides for a second additional memory area (130), in which all physical addresses (136, 138) of the memory pages (128) are stored, to be set up in a main memory (120) of an arithmetic-logic unit. During data transfer the requisite physical addresses are transferred autonomously.

(Figure 5)